IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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March 30, 2004 (Date of Deposit)

Harold C. Moore

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March 30, 2004

Date of Signature

Re: Application of:

Serial No.:

Charles E. May 09/943,196

Filed:

August 30, 2001

Confimration No.:

8500

For:

Arrangement and Method for

Fabricating a Semiconductor Wafer

Group Art Unit:

1765

Examiner:

Lynette T. Umez Eronini

Our Docket No.:

1003-0606 (01-146)

PETITION FOR EXTENSION OF TIME PURSUANT TO 37 C.F.R. § 1.136(a)

Sir:

In connection with the Notice of Non-compliant Amendment mailed February 20, 2004 in the above-entitled patent application, Applicant respectfully requests a one (1) month extension in which to file a response, from March 20, 2004 to April 20, 2004.

04/05/2004 CCHAU1

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01 FC:1251

110.00 OP

The \$110.00 fee required by 37 C.F.R. § 1.136(a) is submitted herewith.

Respectfully Submitted,

Harold C. Moore Attorney for Applicant Registration No. 37,892

March 30, 2004 Maginot, Moore & Beck, LLP Bank One Center/Tower 111 Monument Circle, Suite 3000 Indianapolis, IN 46204-5130 (317) 638-2922